

Phase Distortion Mechanism of a GaAs FET Power Amplifier for Digital Cellular Application

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A conventional class AB amplifier with high efficiency has had phase distortion which is not suitable for $\pi/4$ -shift QPSK signal. A simple FET model including four non-linear elements is presented to explain the distortion mechanism. By using those parameters, a class AB amplifier with low phase variation of less than 2 degrees is designed.

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